



Attorney Docket No. 826.1921

cojc

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Patent Application of:

Yoshiki Okumura

Application No.: 10/781,634

ATTENTION

Confirmation No.: 4130

CERTIFICATE OF CORRECTION

Filed: February 20, 2004

BRANCH

U.S. Patent No.: 7,480,691

Issued: January 20, 2009

For: ARITHMETIC DEVICE FOR MULTIPLE PRECISION ARITHMETIC FOR
MONTGOMERY MULTIPLICATION RESIDUE ARITHMETIC

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate
FEB 24 2009
of Correction

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Patentee respectfully requests that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistakes shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date:

2/20/9

By:

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 7,480,691

DATED : January 20, 2009

INVENTOR(S) : Yoshiki Okumura

Column 11, Line 44, after "has" insert --an--.

Column 12, Line 1, change "value of specifying" to --value of i specifying--.

Column 17, Line 36, after "y_i" insert --as--.

Column 17, Line 61, change "result wherein" to --result, wherein--.

Column 18, Lines 41-44, change "by the value of a selection circuit connected to the partial product generation circuit and the encoder circuit to select and output one of the plurality of partial products according to the selection signal;" to
--by the value of i;
a selection circuit connected to the partial product generation circuit and the
encoder circuit to select and output one of the plurality of partial products
according to the selection signal;--.

Column 19, Line 55, after "N" insert --as--.

Column 20, Line 44, after "N" insert --as--.

Column 20, Line 45, after "block" insert --as--.

Column 20, Line 54, after "D" insert --as--.

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PATENT NO. 7,480,691

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